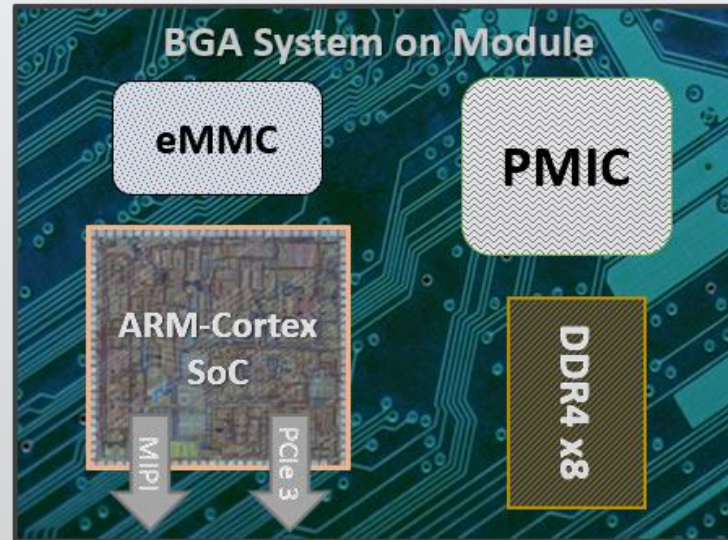


TIEplus 2023 – Subject brief



- The diagram below describes the simplified architecture of a System-on-Module PCB that is based on an ARM-Cortex SoC



- Your task is to ensure the signal integrity of the DDR4 interface considering also the influence of the power delivery network
- Required skills: s-parameter (touchstone file) analysis and processing; timing analysis; full-wave solver extraction; transient simulation; IO modeling; crosstalk analysis; eye-diagram analysis; power-aware IBIS modeling; SI-PI co-simulation